PART C — $(5 \times 8 = 40 \text{ marks})$

Answer ALL questions, choosing either (a) or (b) Each answer should not exceed 600 words.

16. (a) Differentiate large and medium - size microcomputers.

Or

- (b) Discuss about any eight 8085 instructions.
- 17. (a) What are the types of memory? Discuss any two.

Or

- (b) Explain about address decoding and memory address diagram.
- 18. (a) Explain about branch operations in 8085.

Or

- (b) Define dynamic debugging. Discuss it.
- 19. (a) Define stack. Write its instructions.

Or

- (b) Explain about hexadecimal counter.
- 20. (a) What is BCD number? Explain with example of BCD to LED code conversion.

Or

(b) Enumerate about BCD addition with example

Page 4 Code No.: 20516 E

Reg. No.	. :	****************************
----------	-----	------------------------------

Code No.: 20516 E Sub. Code: CSCA 41

B.C.A. (CBCS) DEGREE EXAMINATION, NOVEMBER 2023.

Fourth Semester

Computer Application

Skill Based Subject — MICRO PROCESSOR

(For those who joined in July 2021-2022)

Time: Three hours Maximum: 75 marks

PART A $-(10 \times 1 = 10 \text{ marks})$

Answer ALL questions.

Choose the correct answer:

- 1. Which programming register is a memory pointer?
 - (a) Program counter
- (b) Accumulator

(c) Flags

- (d) None
- 2. The ALU includes flip-flops.
 - (a) three

(b) ten

(c) eight

(d) five

3.	The 8085 microprocessor s	rignals can be classified	10.	from the memory location to the		
	(c) five (d) three) six		(a)	Accumulator (b) Decimal ASCII (d) None	
4.	The data bus and the low order address bus are				PART B — $(5 \times 5 = 25 \text{ marks})$	
	(a) multiplexed (b) (c) fetched (d)) demultiplexed) none of these	À	Ea	er ALL questions, choosing either (a) or (b). Ich answer should not exceed 250 words.	
5.	Choose the arithmetic following	instruction from the	11.	(a)	Write about 8085 assembly programming model.	
	(a) ADD R (b) MUI R) XRA R		(b)	Or Write short notes on micro controllers.	
6.	Logic operation rotate instructions?	es has now many	12.	(a)	Write about advances in memory technology. Or	
	(a) four (b) two		(b)	Explain about (i) ROM (ii) EPROM.	
7.	The accuracy of the time	d) six e delay depends on the	13.	(a)	What are the addressing modes in assembly program? Write it.	
	(5)	o) time d) count		(b)	Write about 16 bit data transfer to register pairs (LXI).	
8.	PSW stands for (a) Program Storage Word	- d	14.	(a)	What is subroutine? How it is implemented? Or	
	(b) Program Status Word(c) Program Secure World(d) None	1		(b)	How restart, conditional call and return instructions are executed?	
).	1 /	r-based products the	15.		Discuss about BCD to binary conversion. Or	
	(a) binary (l	o) hexa d) none		(b)	Write short notes on multiplication technology used in subtraction with carry.	
	Page 2	Code No. : 20516 E			Page 3 Code No. : 20536 E	