

RF Design Engineer 80-100%

Diramics is a high-tech startup company and an ETH Spin-Off, founded in 2016. We develop and fabricate transistors and circuits for high-performance low noise amplifiers. Our products are currently used by leading institutes such as NASA's JPL for deep-space communication, or radio astronomy and quantum computing researchers for their cutting-edge infrastructure.

We are working on making this technology available to users in more commercial markets (eg. satellite- and tele-communication). For this, we are seeking an RF design engineer to join our team and to contribute to the development of low noise amplifiers using our ultra-low noise transistor technology.

Do you want to join a fast-moving start-up where your contributions will make a difference?
Then join us!

Responsibilities

- Design and assembly of ultra-low noise amplifiers
- Characterization and testing of transistors and circuits
- Development and implementation of RF-packaging solutions
- Help prepare and inspect products for shipment to customers
- Support the founders in various other tasks depending on interest and expertise

Qualifications

- Degree in Electrical Engineering or related field
- MMIC design experience using commercial foundries (SiGe and/or GaAs)
- Proficiency with RF design principles and tools (eg. S-Parameters, Keysight ADS or Microwave Office, ...)
- Fluent in English, both written and spoken (German desirable)

Advantageous:

- Experience in design and fabrication of high frequency electronic assemblies
- Experience with test equipment (eg. parameter analyzer, vector network analyzer)
- Knowledge in packaging of semiconductor components
- Programming skills

Benefits

- Influence and be part of a growing high-tech start-up company
- Flexible working hours
- A high level of independence and responsibility from the start
- Constantly evolving challenges and opportunities
- Be part of international collaborative projects

Contact

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